

VISVESVARAYA TECHNOLOGICAL UNIVERSITY, BELGAUM
CHOICE BASED CREDIT SYSTEM (CBCS)
SCHEME OF TEACHING AND EXAMINATION 2016-2018

M.Tech in DIGITAL ELECTRONICS / ELECTRONICS

(Common to M.Tech in Digital Electronics and M.Tech in Electronics)

I SEMESTER

Sl. No	Subject Code	Title	Teaching Hours /Week		Examination				Credit
			Theory	Practical/Field Work/Assignment	Duration	I.A. Marks	Theory/Practical Marks	Total Marks	
1	16ELD11	Advanced Engineering Mathematics	4	-	3	20	80	100	4
2	16EVE12	Digital VLSI Design	4	-	3	20	80	100	4
3	16EVE13	Advanced Embedded System	4	-	3	20	80	100	4
4	16ELD14	Digital Circuit and Logic Design	4	-	3	20	80	100	4
5	16EXX15X	Elective-1	3	-	3	20	80	100	3
6	16ELDL16	Digital Electronics Lab -1		3	3	20	80	100	2
7	16ELD17	Seminar on advanced topics from refereed journals	-	3	-	100	-	100	1
TOTAL			19	6	18	220	480	700	22

Elective-1	
16EVE151	Digital System Design using Verilog
16EVE152	Nanoelectronics
16EVE153	ASIC Design
16ELD154	Advanced Computer Architecture

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II SEMESTER

Sl. No	Subject Code	Title	Teaching Hours /Week		Examination			Credit	
			Theory	Practical/Field Work/Assignment	Duration	I.A. Marks	Theory/Practical Marks		Total Marks
1	16ECS21	Advanced DSP	4	-	3	20	80	100	4
2	16ECS22	Error Control Coding	4	-	3	20	80	100	4
3	16EVE23	Advances in VLSI Design	4	-	3	20	80	100	4
4	16EVE24	Real Time Operating System	4	-	3	20	80	100	4
5	16EXX25X	Elective -2	3	-	3	20	80	100	3
6	16ELDL26	Digital Electronics Lab - 2		3	3	20	80	100	2
7	16ELD27	Seminar on Advanced topics from refereed journals	-	3	-	100	-	100	1
TOTAL			19	6	18	220	480	700	22

Elective-2	
16ELD251	Automotive Electronics
16ECS252	Multimedia Over Communication Links
16ELD253	MEMS
16ECS254	Cryptography and Network Security

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III SEMESTER: Internship

Sl. No	Subject Code	Title	Teaching Hours /Week		Examination			Credit	
			Theory	Practical/Field Work/Assignment	Duration	I.A. Marks	Theory/Practical Marks		Total Marks
1	16ELD31	Seminar / Presentation on Internship (After 8 weeks from the date of commencement)	-	-	-	25	-	25	20
2	16ELD32	Report on Internship	-	-	-	25	-	50	
3	16ELD33	Evaluation and Viva-Voce of Internship	-	-	-	-	50	50	
4	16ELD34	Evaluation of Project phase -1	-	-	-	50	-	25	1
TOTAL			-	-	-	100	50	150	21

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IV SEMESTER

Sl. No	Subject Code	Title	Teaching Hours /Week		Examination				Credit
			Theory	Practical/Field Work/ Assignment	Duration	I.A. Marks	Theory/ Practical Marks	Total Marks	
1	16ELD41	Synthesis and optimization of Digital Circuits	4	-	3	20	80	100	4
2	16EXX42X	Elective-3	3	-	3	20	80	100	3
3	16ELD43	Evaluation of Project phase -2	-	-	-	50	-	50	3
4	16ELD44	Evaluation of Project and Viva-Voce	-	-	-	-	100+100	200	10
TOTAL			-	-	6	90	360	450	20

Elective-3	
16EVE421	CMOS RF Circuit Design
16ECS422	Advances in Image Processing
16ECS423	Communication System Design using DSP Algorithms
16ELD424	Reconfigurable Computing

Note:

- 1. Project Phase-1:** 6-week duration shall be carried out between 2nd and 3rd Semester vacation. Candidates in consultation with the guide shall carry out literature survey/ visit industries to finalize the topic of Project.
- 2. Project Phase-2:** 16-week duration during 4th semester. Evaluation shall be done by the committee constituted comprising of HoD as Chairman, Guide and Senior faculty of the department.
- 3. Project Evaluation:** Evaluation shall be taken up at the end of 4th semester. Project work evaluation and Viva-Voce examination shall be conducted .
 - a. Internal Examiner shall carry out the evaluation for 100 marks.
 - b. External Examiner shall carry out the evaluation for 100 marks.
 - c. The average of marks allotted by the internal and external examiner shall be the final marks of the project evaluation.
 - d. Viva-Voce examination of Project work shall be conducted jointly by Internal and External examiner for 100 marks.