Name: Ramesh Bantwal	Photo:
Qualification: M.E, Pursuing PhD	
Designation: Associate Professor	
	Research Interests:
Department : Electrical & Electronics Engineering	Wireless Sensor Networks, IoT, Protection
	Recent Publications:
Membership : ISTE - LM 7708 IEEE - 9254 3315	 FPGA Implementation of best suitable video compression with respect to region of interest.(2016) VLSI implementation of built in Self repair for faulty Memory.(2015) Upgrading substation relays to digital reclosers and their coordination with sectionalizers.(2015)
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Technical skills: Labview Programming PLC PSCAD programming	